

Refine Search

Search Results -

Terms	Documents
L47 and encod\$3	30

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L48

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, November 21, 2006 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L48</u>	L47 and encod\$3	30	<u>L48</u>
<u>L47</u>	L46 and L45	45	<u>L47</u>
<u>L46</u>	341/\$.ccls.	40381	<u>L46</u>
<u>L45</u>	L44 and L43	149	<u>L45</u>
<u>L44</u>	complement\$ with binary with cod\$3	1805	<u>L44</u>
<u>L43</u>	conventional with binary with cod\$3	2791	<u>L43</u>
<u>L42</u>	L39 and L38	4	<u>L42</u>
<u>L41</u>	L39 same L38	2	<u>L41</u>
<u>L40</u>	L39 same L38	2	<u>L40</u>
<u>L39</u>	((complement\$ adj binary) adj cod\$3)	189	<u>L39</u>
<u>L38</u>	(conventional adj (binary adj cod\$3))	444	<u>L38</u>
<u>L37</u>	L34 and (encod\$3 adj table)	26	<u>L37</u>
<u>L36</u>	L34 and (encod\$3 adj table)	26	<u>L36</u>

<u>L35</u>	L34 and (encod\$3 adj table)	26	<u>L35</u>
<u>L34</u>	L24 and L30	388	<u>L34</u>
<u>L33</u>	L29 and (encod\$ adj table)	0	<u>L33</u>
<u>L32</u>	L31 and L29	0	<u>L32</u>
<u>L31</u>	L30 and L30	40381	<u>L31</u>
<u>L30</u>	341/\$.ccls.	40381	<u>L30</u>
<u>L29</u>	L26 and encod\$3	40	<u>L29</u>
<u>L28</u>	L26 and encod\$3	40	<u>L28</u>
<u>L27</u>	L26 and ecnod\$3	0	<u>L27</u>
<u>L26</u>	L25 and L24	44	<u>L26</u>
<u>L25</u>	imprint\$3	63774	<u>L25</u>
<u>L24</u>	"0000" and "1111" and "0001" and "1110"	4395	<u>L24</u>
<u>L23</u>	(encod\$3 adj table) adj ("four-bit" or (four adj bit\$1) or "4-bit")	16	<u>L23</u>
<u>L22</u>	L21 with count\$3	32	<u>L22</u>
<u>L21</u>	(encod\$3 near2 "four-bit") or (encod\$3 near2 (four adj bit)) or (encod\$ adj (table2 adj "4-bit"))	1155	<u>L21</u>
<u>L20</u>	"four-bit" or (four adj bit) or (encod\$ adj (table2 adj "4-bit"))	48176	<u>L20</u>
<u>L19</u>	encod\$3 same input\$ same (switch\$ or multiplex\$3 oe select\$3) same bit\$1 same logical same count\$3 same even same odd	17	<u>L19</u>
<u>L18</u>	L15 and encod\$3	17	<u>L18</u>
<u>L17</u>	L15 and encod\$3	17	<u>L17</u>
<u>L16</u>	L15 and L1	0	<u>L16</u>
<u>L15</u>	L14 and (L2 or "2-1 multiplexer")	38	<u>L15</u>
<u>L14</u>	"xnor"	3209	<u>L14</u>
<u>L13</u>	L8 and L11 and (L2 or "2-1 multiplexer")	9	<u>L13</u>
<u>L12</u>	L11 and L10	0	<u>L12</u>
<u>L11</u>	341/\$.ccls.	40381	<u>L11</u>
<u>L10</u>	L8 and L7	16	<u>L10</u>
<u>L9</u>	L8 and L7	16	<u>L9</u>
<u>L8</u>	"nor"	842596	<u>L8</u>
<u>L7</u>	L6 and L4	28	<u>L7</u>
<u>L6</u>	logic adj gat\$1	29456	<u>L6</u>
<u>L5</u>	"logic adj gate"	0	<u>L5</u>
<u>L4</u>	L2 and encod\$3	164	<u>L4</u>
<u>L3</u>	L2 with encod\$3	7	<u>L3</u>
<u>L2</u>	"2 to 1 multiplexer" or "two-to-one multiplexer"	604	<u>L2</u>
<u>L1</u>	(encod\$3 adj table) adj "4-bit"	16	<u>L1</u>

END OF SEARCH HISTORY